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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/541,827	07/11/2005	Jan Tuma	49126	4278
1609	7590	03/17/2010		
ROYLANCE, ABRAMS, BERDO & GOODMAN, L.L.P. 1300 19TH STREET, N.W. SUITE 600 WASHINGTON, DC 20036			EXAMINER	
			NORRIS, JEREMY C	
			ART UNIT	PAPER NUMBER
			2841	
			MAIL DATE	DELIVERY MODE
			03/17/2010	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

<b>Office Action Summary</b>	<b>Application No.</b> 10/541,827	<b>Applicant(s)</b> TUMA, JAN
	<b>Examiner</b> Jeremy C. Norris	<b>Art Unit</b> 2841

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --  
**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED. (35 U.S.C. § 133).

Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

#### Status

1) Responsive to communication(s) filed on 22 December 2009.

2a) This action is FINAL.      2b) This action is non-final.

3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

#### Disposition of Claims

4) Claim(s) 15-34 is/are pending in the application.

4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.

5) Claim(s) \_\_\_\_\_ is/are allowed.

6) Claim(s) 15-34 is/are rejected.

7) Claim(s) \_\_\_\_\_ is/are objected to.

8) Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

#### Application Papers

9) The specification is objected to by the Examiner.

10) The drawing(s) filed on 11 July 2005 is/are: a) accepted or b) objected to by the Examiner.  
 Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
 Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

#### Priority under 35 U.S.C. § 119

12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

a) All    b) Some \* c) None of:

1. Certified copies of the priority documents have been received.
2. Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

#### Attachment(s)

1) Notice of References Cited (PTO-892)

2) Notice of Draftsperson's Patent Drawing Review (PTO-948)

3) Information Disclosure Statement(s) (PTO/GS-68)  
 Paper No(s)/Mail Date \_\_\_\_\_

4) Interview Summary (PTO-413)  
 Paper No(s)/Mail Date \_\_\_\_\_

5) Notice of Informal Patent Application

6) Other: \_\_\_\_\_

#### **DETAILED ACTION**

##### ***Claim Rejections - 35 USC § 102***

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 15-20 and 29-34 are rejected under 35 U.S.C. 102(b) as being anticipated by US 4,429,348 (Dean),

Dean discloses, referring primarily to figures 1 & 3, an adhesive closure part, comprising: a flat carrier (16) having first (32) and second (14) opposite surfaces; adhesive closure elements of electrically insulating plastic projecting from said first surface of said flat carrier and comprising at least one of hooks, mushroom-shaped members and loops (40, col. 2, lines 45-55); and a circuit directly on said second surface of said flat carrier, said circuit including at least one of an electrical component and electronic component (20, col. 5-15) [claim 15], wherein another electrical or electronic component (22) is located one of in and directly on said flat carrier [claim 16], wherein another electrical or electronic component (22) is integrated into said flat carrier (col. 2, lines 5-15) [claim 17], wherein said one of the electrical component and electronic component comprises an application of one of thick and thin film technology [claim 18], wherein another of an electrical or electronic component (22) is laminated onto said flat carrier [claim 19], wherein said circuit comprises electrical conductor strips [claim 20], wherein said flat carrier and said adhesive closure elements are of at least

one of the group consisting of duroplastic, thermoplastic, polymer plastic and acrylate plastic [claim 29], wherein said circuit comprises printed electrical conductors (30) directly on said flat carrier and connected to an electronic component (20, col. 2, lines 1-20) [claim 30], wherein said circuit comprises printed electrical conductors (30) directly on said flat carrier connected to an electronic component integrated in said flat carrier (col. 2, lines 1-20) [claim 31], wherein said circuit comprises printed electrical conductors (30) directly on said flat carrier connected to and extending from an electric component (20) laminated directly on said flat carrier and connected to said printed electrical conductors [claim 32], wherein said circuit comprises first and second electrical conductors (30) directly on said flat carrier having adjacent ends abutting one another when said carrier is in one position and separated from one another when said flat carrier is in another position [claim 33], wherein said flat carrier is flexible and electrically insulating [claim 34].

***Claim Rejections - 35 USC § 103***

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

The factual inquiries set forth in *Graham v. John Deere Co.*, 383 U.S. 1, 148 USPQ 459 (1966), that are applied for establishing a background for determining obviousness under 35 U.S.C. 103(a) are summarized as follows:

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1. Determining the scope and contents of the prior art.
2. Ascertaining the differences between the prior art and the claims at issue.
3. Resolving the level of ordinary skill in the pertinent art.
4. Considering objective evidence present in the application indicating obviousness or nonobviousness.

Claims 21, 27, and 28 are rejected under 35 U.S.C. 103(a) as being unpatentable over Dean.

Regarding claim 21, Dean discloses the claimed invention as described above except Dean does not specifically disclose that the circuit has electrical or electronic sensors [claim 21]. However, it is well known in the art to add sensors to an electronic circuit of which fact the Examiner takes Official Notice. Therefore, it would have been obvious to one having ordinary skill in the art at the time of invention to add a sensor to the invention of Dean. The motivation for doing so would have been to allow the circuit to sense an outside stimulus.

Regarding claims 27-28, Dean discloses the claimed invention as described above except Dean does not specifically disclose wherein the circuit has an energy storage device [claim 27] wherein said energy storage device is an electrochemical storage device being an application of thin or thick film technology [claim 28]. However, it is well known in the art to provide chemical batteries in a circuit of which fact the Examiner takes Official Notice. Therefore, it would have been obvious to one having ordinary skill in the art at the time of invention to add a chemical battery to the invention of Dean. The motivation for doing so would have been to power the electrical circuit. Additionally, the limitation "in thin or thick film technology" is a process limitation in a product claim and thus has been considered only to the extent that said process

impacts the structure of the device. Moreover, it has been held "even though product-by-process claims are limited by and defined by the process, determination of patentability is based on the product itself. The patentability of a product does not depend on its method of production. If the product in the product-by-process claim is the same as or obvious from a product of the prior art, the claim is unpatentable even though the prior product was made by a different process. *In re Thorpe*, 777 F.2d 695, 698, 227 USPQ 964, 966 (Fed. Cir. 1985).

Claims 22-26 are rejected under 35 U.S.C. 103(a) as being unpatentable over Dean in view of US 6,173,899 B1 (Rozin).

Dean discloses the claimed invention as described above except Dean does not specifically disclose that said circuit comprises an integrated semiconductor component [claim 22]. However, it is well known in the art to include memory with an IC and a coil for contactless reading in a circuit board as evidenced by Rozin (col. 2, lines 45-60 and col. 3, lines 10-25). Therefore, it would have been obvious to one having ordinary skill in the art at the time of invention to include memory with the IC and a coil for contactless reading in the board of Dean as is known in the art and evidenced by Rozin. The motivation for doing so would have been to allow the device of Dean to receive, process, and store signals. Additionally, the modified invention of Dean teaches that said integrated semiconductor component comprises an electronic data memory (Rozin col. 2, lines 45-60 and col. 3, lines 10-25) [claim 23], wherein said electronic data memory stores data readable without contact (Rozin col. 1, lines 5-15) [claim 24],

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wherein said data is storables without contact (Rozin col. 1, lines 5-15) [claim 25],  
wherein said circuit comprises at least one receiving coil for receiving electrical energy  
to operate said circuit by an electromagnetic field [claim 26].

***Response to Arguments***

Applicant's arguments with respect to claims 15-34 have been considered but are moot in view of the new ground(s) of rejection.

***Conclusion***

Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jeremy C. Norris whose telephone number is (571)272-1932. The examiner can normally be reached on Monday - Thursday, 8:00 am - 6:00 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Jinhee J. Lee can be reached on 571-272-1977. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Jeremy C. Norris  
Primary Examiner  
Art Unit 2841

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